

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Err Ors
1	BRS	L1	1	"10666968"	USPAT ; US-PG PUB	2004/09/15 16:28			0
2	BRS	L2	703	logic & event & parameter & identification & actual & duration & comparing & interface & peripheral & host & bus & processor	USPAT ; US-PG PUB	2004/09/15 16:28			0
3	BRS	L3	2	377/13 & logic & event & parameter & identification & actual & duration & comparing & interface & peripheral & host & bus & processor	USPAT ; US-PG PUB	2004/09/15 16:29			0
4	BRS	L4	5	377/15 & logic & event & parameter & identification & actual & duration & comparing & interface & peripheral & host & bus & processor	USPAT ; US-PG PUB	2004/09/15 16:30			0
5	BRS	L5	3	377/16 & logic & event & parameter & identification & actual & duration & comparing & interface & peripheral & host & bus & processor	USPAT ; US-PG PUB	2004/09/15 16:30			0
6	BRS	L6	2	370/392 & logic & event & parameter & identification & actual & duration & comparing & interface & peripheral & host & bus & processor & inter-arrival & time queue & histogram	USPAT ; US-PG PUB	2004/09/15 16:30			0
7	BRS	L7	1	702/60 & logic & event & parameter & identification & actual & duration & comparing & interface & peripheral & host & bus & processor	USPAT ; US-PG PUB	2004/09/15 16:31			0
8	BRS	L8	1	702/67 & logic & event & parameter & identification & actual & duration & comparing & interface & peripheral & host & bus & processor	USPAT ; US-PG PUB	2004/09/15 16:31			0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
9	BRS	L9	1	702/178 & logic & event & parameter & identification & actual & duration & comparing & interface & peripheral & host & bus & processor	MSPAT ; US-PG & PUB	2004/09/15 16:31			0
10	BRS	L10	2	702/179 & logic & event & parameter & identification & actual & duration & comparing & interface & peripheral & host & bus & processor	MSPAT ; US-PG & PUB	2004/09/15 16:31			0
11	BRS	L11	1	702/181 & logic & event & parameter & identification & actual & duration & comparing & interface & peripheral & host & bus & processor	MSPAT ; US-PG & PUB	2004/09/15 16:31			0
12	BRS	L12	2	702/182 & logic & event & parameter & identification & actual & duration & comparing & interface & peripheral & host & bus & processor	MSPAT ; US-PG & PUB	2004/09/15 16:31			0
13	BRS	L13	2	702/186 & logic & event & parameter & identification & actual & duration & comparing & interface & peripheral & host & bus & processor	MSPAT ; US-PG & PUB	2004/09/15 16:31			0
14	BRS	L14	3	702/188 & logic & event & parameter & identification & actual & duration & comparing & interface & peripheral & host & bus & processor	MSPAT ; US-PG & PUB	2004/09/15 16:32			0